

PATENT APPLICATION Docket No. 8750-040 Client No. SPX200211-0043

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor:

Jae-Hun Kim and Kyu-Whan Chong

For:

MOS TRANSISTOR HAVING A RECESSED GATE ELECTRODE

AND FABRICATION METHOD THEREOF

Applicant requests FIG. 3A to be published with the application.

Enclosures:

Specification (pages 1-9); claims (pages 10-12); abstract (page 13)

Six sheet(s) of FORMAL drawings (comprising 12 figures)

Certified copy of Priority Document No. 2002-79629, filed December 13, 2002

PTO form 1449 Information Disclosure Statement listing 1 cited U.S. Patent

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CLAIMS AS FILED					
For	Number Filed	Number Extra	Rate		Basic Fee \$750
Total Claims	15-20	0	x \$ 18	=	\$0.00
Independent Claims	2-3	0	x \$ 84	=	\$0.00
TOTAL FILING FEE					\$750.00

Samsung electronics Co., Ltd.

Suwon-city, Kyungki-do Republic of Korea

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Respectfully submitted,

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